



RF Engines Ltd,
Innovation Centre
St Cross Business Park
Newport
Isle of Wight
PO30 5WB
Tel +44 (0)1983 550330
Fax +44 (0)1983 550340
E-Mail Info@rfel.com

RF Engines Ltd sponsors two MSc awards at York University

Newport, Isle of Wight, UK – February 24, 2009 -- RF Engines Limited (RFEL), who specialise in high performance signal processing design work, is sponsoring the 2009 MSc in DSP programme at the University of York. The company is providing two special awards of £1500 each that will be presented to graduates of the course -- firstly for 'excellence in theoretical signal processing' and secondly for 'excellence in signal processing design'.

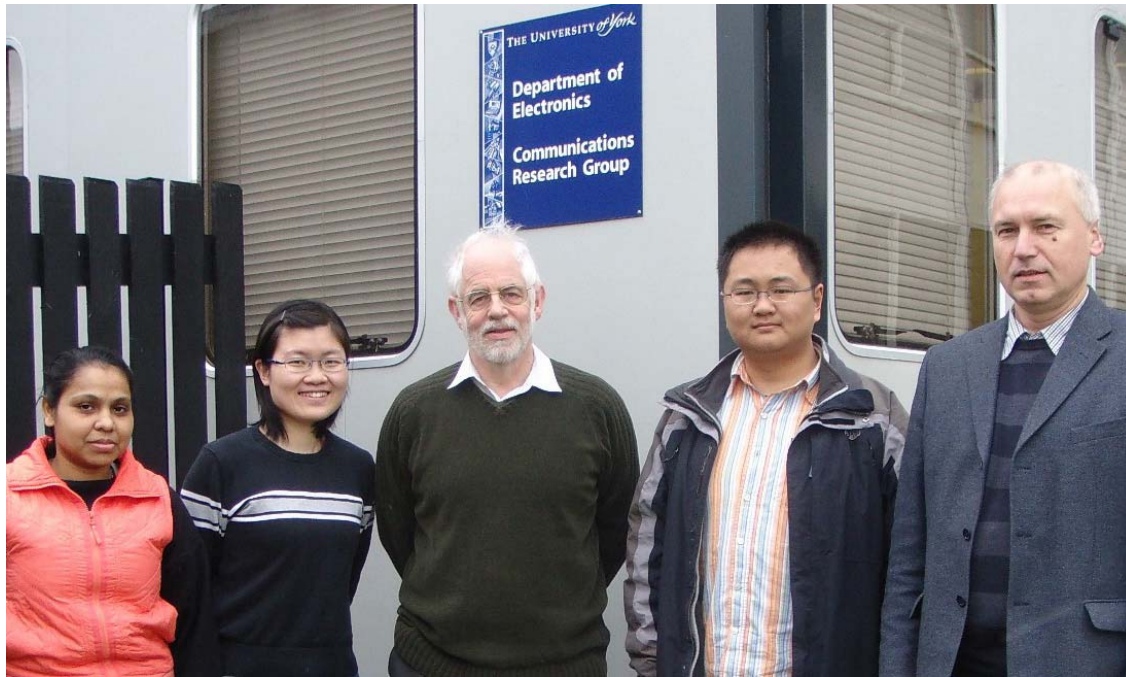
"Over the past couple of years, we have developed a close working relationship with the University of York's Department of Electronics," said John Summers, RFEL's CEO. "In fact, several of our staff are graduates of the University's programme in signal processing, which focuses on the latest techniques in FPGA-based digital design and is probably one of the best programmes in the UK for training students in advanced DSP techniques."

Doctor Yuriy Zakharov, Leader of the DSP programme, added, "It is vitally important to provide students with hands-on experience of the challenges faced in the real world by working closely with industry. RF Engines is one of the leading companies in the area of designing solutions for challenging digital signal processing applications and we are delighted that they share their expertise and experiences."

The one-year full-time taught MSc programme in Digital Signal Processing (DSP) starts in October 2009 at York University. This programme will teach the theoretical foundations of DSP methods and develop skills in practical implementation techniques such as FPGA design. The programme uses the expertise of all the research groups in the Department of Electronics to cover state-of-the-art applications of DSP techniques to the fields of communications, instrumentation, bio-engineering, medical sensing, imaging and multimedia, amongst others. The MSc in DSP is designed for graduates in mathematics, engineering, physics or computer science, and will provide a good route to a career in any of the varied industries that use DSP technology for students with any of these backgrounds. For more details, visit the MSc in Digital Signal Processing web-pages at <http://www.elec.york.ac.uk/gsp/mscDigSig.html>

Illustration

The attached picture shows members of the Communications Research Group at the University of York who are involved in FPGA design of signal processing algorithms



(from the left):
Sasmita Deo, MPhil student;
Zhi Quan, PhD student;
Tim Tozer, Head of the Communications Research Group,
Jie Liu, PhD student;
Dr. Yuriy Zakharov, Reader in Signal Processing for Communications

RF Engines Ltd

RF Engines Limited (RFEL) is a UK-based electronic systems designer, providing high specification signal processing solutions for FPGAs, as well as receiver and complete product solutions for the homeland security, defence, communications and instrumentation markets. Applications include communications base stations, satellite communications systems, test and measurement instrumentation, and bespoke wideband receivers/transceivers.

Further Information

RF Engines Limited

Web: www.rfel.com

Email: info@rfel.com

Tel: +44 (0) 1983 550330

Press Information

Nigel Robson - Vortex PR

Web: www.vortexpr.com

Email: nigel@vortexpr.com

Tel: +44 (0) 1481 233080