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RF Engines Grant Development Licence to Advanced Architectures

PFT being developed for ultra low power applications

RF Engines (RFEL) has announced the granting of a development licence for its **Pipelined Frequency Transform (PFT)** architecture to Advanced Architectures (A2) of Irvine, California. The joint development will utilise A2's experience in low power ASIC design to produce an extremely low power (only tens of mW) versions of the PFT technology for use in various applications.

One of the first projects will be an interference rejection chip for communication and GPS systems. The combination of the PFT's unique filter characteristics and architectural efficiency will allow multiple narrow band interferers to be removed from the spectrum by use of an adaptive filtering technique running on A2's customisable A2MP parallel DSP core.

The low power design will significantly increase the reliability and effectiveness of mobile GPS applications needed for the future, location-based services market.

This development is just one of the many application areas opening up for RFEL's newly released PFT technology that provides the ability to process a wide slice of the spectrum (up to 100MHz) in real time.

John Summers of RFEL commented: "We are extremely pleased to be working with A2 on low power versions of the PFT as it will open up an even wider market for the architecture. Following on from the GPS application, we expect to generate general purpose, low power designs for OFDM and mobile wireless application."

Roger Thorpe of A2 commented: "The combination of A2's parallel A2MP DSP and RFEL's innovative PFT architecture is an excellent match for

developing signal processing solutions within the frequency domain. Our experience with low power ASIC design further enhances this capability by generating designs deployable in portable systems.”

Advanced Architectures

Advanced Architectures (A2) is a leading design house specialising in developing customised architectures for customer solutions. The company also offers a growing range of silicon-proved Intellectual Property (IP) cores for the rapid development of system-on-chip solutions. These IP cores are designed to improve the design time-to-market delay, eliminate design risks, and reduce development costs. Founded in 1993, company has a strong customer base in the United States. Further information on A2 can be found at www.a-2.com, phoning on +1 949 854 4390 or e-mail info@a-2.com.

RF Engines

RF Engines Limited (RFEL) is a UK based designer of high specification signal processing cores, sold as intellectual property (IP) for inclusion in SoC and semiconductor devices in the defence, communications and instrumentation markets. More specifically it is a solutions provider for projects requiring complex front end, real time, wide and narrow band, flexible channelisation. RFEL provides a range of standard cores covering multiple FFT and unique PFT techniques, as well as design services for specialist applications.

Further information on RFEL can be found at www.rfel.com, phoning on +44 (0) 1983 550326 or e-mail info@rfel.com.

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